

10/15/01	Class	Subclass	ISSUE CLASSIFICATION

O.I.P.E.	PATENT DATE
MA SCANNED AS 2 O.A.	

APPLICATION NO. 09/977787	CONT/PRIOR F	CLASS 714	SUBCLASS 719	ART UNIT 2133	EXAMINER <i>[Signature]</i>
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## Trimming

Circuit and method for testing a data memory

**TITLE**


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PTO-2040  
12/99

**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket

 <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> a) The term of this patent (but subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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